



(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/790,522
Filing Date	03/01/2004
First Named Inventor	Seth M. Demsey
Art Unit	2154
Examiner Name	Unknown
Attorney Docket Number	MS306914.1/MSETP571US

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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Examiner Signature	/Parham Fatehi/	Date Considered	09/24/2007
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		Application Number	10/790,522		
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Sheet	2	of	2	Attorney Docket Number	MS306914.1/MSFTP571US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/P.F./		HOWARD CHEN, et al., Dynamic Trace Selection Using Performance Monitoring Hardware Sampling, Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization, 2003, pp. 79-90, San Francisco, California, USA.	
↓		EHAB AL-SHAER, et al., Adaptive Object-Oriented Filtering Framework for Event Management Applications, ACM Computing Surveys, 2000, Vol. 32-Issue 1es.	
↓		MATTHEW FARRENS, et al., Dynamic Base Register Caching: A Technique for Reducing Address Bus Width, Proceedings of the 18th annual international symposium on Computer architecture, 1991, pp.128-137, Toronto, Ontario, Canada.	

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